

REMARKS

Claims 1-8 are pending in the application and stand rejected by the examiner. Claims 1, 5 and 8 are independent claims. New claims 9-16 have been added. No new matter was added.

Applicants have amended the specification to include the registered trademark symbol, where appropriate. No new matter was added.

Applicants have amended the claims to correct any informalities and to provide clear antecedent basis for all terms used. No new matter was added.

The examiner uses Portanova to reject claims 1 and 5 as having been anticipated.

Claim 1 recites "an execution unit for executing multiple context threads." At least this quoted claim feature is neither disclosed nor suggested in Portanova. Accordingly, claim 1 is not anticipated by Portanova.

Claim 5 recites "processing operands for an executing context thread of multiple context threads through a multiplexor and an arithmetic logic unit." At least this quoted claim feature is neither disclosed nor suggested in Portanova. Accordingly, claim 5 is not anticipated by Portanova.

New independent claim 9 recites "a plurality of microengines." At least this quoted claim feature is neither disclosed nor suggested in Portanova. Accordingly, claim 9 is not anticipated by Portanova.

The examiner uses Portanova and Patterson to reject claim 4 as having been obvious.

Claim 1 is not rendered obvious by Portanova and Patterson. Claim 4 depends upon, and further limits, claim 1. Accordingly, claim 4 is not rendered obvious by Portanova and Patterson.

The examiner uses Portanova and the Intel Architecture Guide to reject claims 2-3 and 6-7 as having been obvious.

Claims 1 and 5 are not rendered obvious by Portanova and the Intel Architecture Guide. Claims 2-3 and 6-7 depend upon, and further limit, claims 1 and 5. Accordingly, claims 2-3 and 6-7 are not rendered obvious by Portanova and the Intel Architecture Guide.

Applicants have canceled claim 8.

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It is believed that all of the pending claims have been addressed. However, the absence of a reply to a specific rejection, issue or comment does not signify agreement with or concession of that rejection, issue or comment. In addition, because the arguments made above may not be exhaustive, there may be reasons for patentability of any or all pending claims (or other claims) that have not been expressed. Finally, nothing in this paper should be construed as an intent to concede any issue with regard to any claim, except as specifically stated in this paper, and the amendment of any claim does not necessarily signify concession of unpatentability of the claim prior to its amendment.

Enclosed is a \$86.00 check for excess independent claims. Please apply any other charges or credits to deposit account 06-1050.

Respectfully submitted,

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